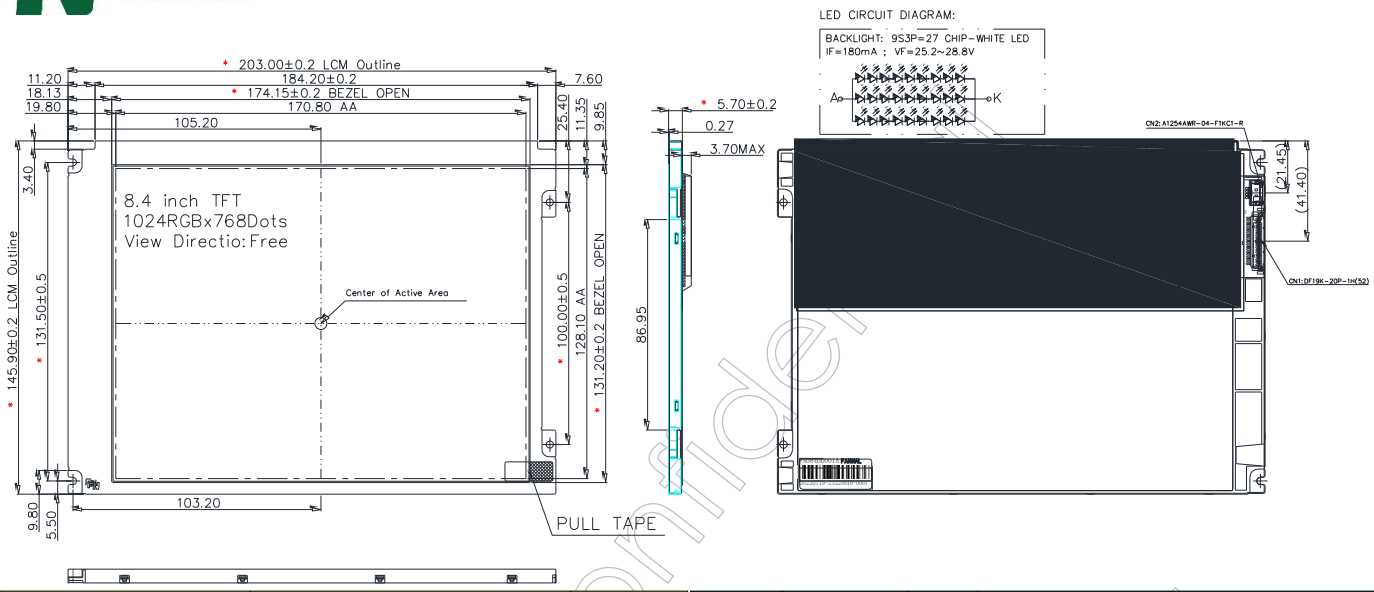
FN0840D001A



Parameter	Specification	Unit
LCD size	8.4(Diagonal)	inch
Resolution Ratio	1024(H)×768(V)	pixels
Pixel Pitch	0.1668(H)×0.1668(V)	mm
Active Area	170.8032(H)×128.1024(V)	mm
Module Size	203.0(W)×145.9(H)×9.4(D)	mm
Display Mode	Normally Black, Transmissive	
Interface	LVDS	
Surface treatment	Anti-Glare	
View Direction	ALL	O'clock
Display colors	16.7M	Colors
Power Supply	3.3	V
Power Consumption	5.0(Typ.)	W
Weight	245(Typ.)	g
Luminance	1000(Typ.)	cd/m²
Driver IC	ST5821+ST5084	
Operating Temperature	-20~+70	℃
Storage Temperature	-30~+80	℃

Parameter	Symbol	Min.	Typ.	Max.	Unit
Power supply voltage	VDD	3.0	3.3	3.6	V
Input logic high voltage	VIH	0.7VDD	-	VDD	V
Input logic low voltage	VIL	0	-	0.3VDD	V
Clock Frequency	FCLK	48.4	52.4	61.5	MHz

Parameter	Symbol	Min.	Typ.	Max.	Unit
Input Voltage	VLED	-0.3	12.0	12.5	V
Input Current	ILED	-	450	600	mA
PWM Duty	Duty	0.18		100	%
PWM Freq	Freq	0.1		20	KHz
EN Input voltage	High	1.3	--	24	V
	Low	--	--	0.5	V
LED Lifetime	-	-	50000	-	Hrs

Pin No.	Symbol	I/O	Description
1-2	VDD	P	Supply analog voltage.
3	SC	I	Scan direction control. (Low=Normal, High=Reverse).
4	NC	-	No connection.
5	RXIN0N	I	LVDS Negative differential data signal.
6	RXIN0P	I	LVDS Positive differential data signal.
7	GND	P	Ground.
8	RXIN1N	I	LVDS Negative differential data signal.
9	RXIN1P	I	LVDS Positive differential data signal.
10	GND	P	Ground.
11	RXIN2N	I	LVDS Negative differential data signal.
12	RXIN2P	I	LVDS Positive differential data signal.
13	GND	P	Ground.
14	RXCLKN	I	LVDS Negative clock signal.
15	RXCLKP	I	LVDS Positive clock signal.
16	GND	P	Ground.
17-18	NC	-	No connection.
19	RXIN3N	I	LVDS Negative differential data signal.
20	RXIN3P	I	LVDS Positive differential data signal.

Pin No.	Symbol	I/O	Description
1	LEDA	P	Power supply for LED anode input. (12.0V)
2	GND	P	Ground.
3	LED_EN	I	LED enable signal.
4	LED_PWM	I	PWM dimming control signal of LED converter.